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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10016194	FILING DATE 11/02/2001	CLASS <del>327</del>	SUBCLASS 377	GAU 2816	EXAMINER FRAN
<b>**APPLICANTS:</b> Patel Ketankumar; 379 2644 BRINEY (3) 379 **CONTINUING DATA VERIFIED:  ** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB DO NOT PUBLISH <input checked="" type="checkbox"/> RESCIND <input type="checkbox"/> Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no ATTORNEY DOCKET NO 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no 01CON231P Verified and Acknowledged Examiners's initials TITLE : Circuit for reducing voltage peak in interfacing with a telephone line <small>U.S. DEPT. OF COMM./PAT. &amp; TM.-PTO-4361 (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner  Primary Examiner		DRAWING Sheets/Drwg. Figs/Drwg. Print/Figs	
ISSUE FEE Amount Due Date Paid		Application Examiner	
TERMINAL DISCLAIMER		PREPARED FOR ISSUE <b>WARNING:</b> The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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